<u>Listing of Claims:</u>

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Original) A method for concurrently acquiring, processing, and transmitting digital video and still images, comprising:

acquiring video frames from one or more image sensors;

processing the video frames using a video pipeline, wherein the video pipeline includes one or more processors;

temporarily storing the video frames in a frame buffer when one or more high resolution still images are acquired during the video frame acquisition; and

processing the high resolution still images using a still image pipeline, wherein the still image pipeline runs concurrently with the video pipeline.

2. (Original) The method of claim 1, wherein the processing the video frames step comprises: downsampling and demosaicing the video frames; and color correcting the video frames.

3. (Orignal) The method of claim 1, wherein the processing the high resolution still images step comprises:

downsampling and demosaicing the high resolution still images using complex demosaicing algorithms; and

color correcting the high resolution still images using complex color correction algorithms.

4. (Original) The method of claim 1, further comprising compressing the video frames and the high resolution still images.

5. (Original) The method of claim 1, further comprising transmitting the video frames and the high resolution still images through communications channels.

6. (Original) The method of claim 1, further comprising storing the video frames and high resolution still images in a storage device.

7. (Original) The method of claim 1, further comprising emptying the frame buffer by the processors after the high resolution still images are processed, transmitted or stored.

8.(Original) The method of claim 1, wherein the processing the high resolution still images step includes processing the high resolution still images using the same image sensors and the same processors in the video pipeline.

9. (Original) The method of claim 1, wherein the processing the video frames step and the processing the high resolution still images step include processing the video frames and the high resolution still images using separate hardware processing pipelines.

10. (Original) A concurrent dual video and still image pipeline for a video camera system, comprising:

one or more image sensors capable of acquiring video frames and high resolution still images, wherein the high resolution still images are acquired during the video frame acquisition;

a sensor controller capable of storing the video frames into a memory;

one or more processors capable of concurrently processing the video frames and the high resolution still images, wherein the video frames are processed using a video pipeline, and the high resolution still images are processed using a still image pipeline, and wherein the video pipeline runs concurrently with the still image pipeline;

a frame buffer capable of temporarily storing the video frames when the high resolution still images are being processed.

11. (Original) The concurrent dual video and still image pipeline of claim 10, further comprising:

a storage device capable of storing the video frames and the high resolution still images.

12. (Original) The concurrent dual video and still image pipeline of claim 10, further comprising:

an input/output unit capable of transmitting the video frames and the high resolution

still images through communications channels.

13. (Original) The concurrent dual video and still image pipeline of claim 10, wherein the frame buffer is emptied after the high resolution still images are processed, transmitted or stored.

14. (Original) The concurrent dual video and still image pipeline of claim 10, wherein the processors are selected from a microprocessor, an application specific integrated circuit (ASIC), and a digital signal processor.

15. (Original) The concurrent dual video and still image pipeline of claim 10, wherein the processors downsample, demosaic, and color correct the video frames.

16. (Original) The concurrent dual video and still image pipeline of claim 10, wherein the processors downsample, demosaic, and color correct the high resolution still images using complex algorithms.

17. (Original) The concurrent dual video and still image pipeline of claim 10, wherein the video pipeline and the still image pipeline use the same image sensors and the same processors.

18. (Original) The concurrent dual video and still image pipeline of claim 10,

wherein the video pipeline and the still image pipeline use separate image sensors and separate hardware processing pipelines.

19. (Original) The concurrent dual video and still image pipeline of claim 10, wherein the video pipeline and the still image pipeline use the same image sensors and separate hardware processing pipelines.

20. (Original) A computer readable medium providing instructions for concurrently acquiring, processing, and transmitting digital video and high resolution still images, the instructions comprising:

acquiring video frames from one or more image sensors;

processing the video frames using a video pipeline, wherein the video pipeline includes one or more processors;

temporarily storing the video frames in a frame buffer when one or more high resolution still images are acquired during the video frame acquisition; and

processing the high resolution still images using a still image pipeline, wherein the still image pipeline runs concurrently with the video pipeline.

21. (Previously Submitted) A method for processing digital video and still images, comprising the steps of:

acquiring video frames from one or more image sensors;

processing the video frames using a video pipeline, wherein the video pipeline includes one or more processors;

storing the video frames in a frame buffer while one or more high resolution still images are acquired during the video frame acquisition; and

processing the high resolution still images using a still image pipeline,

wherein still image processing, including image compression, performed by the still image pipeline on said one or more high resolution images runs concurrently with video processing, including image compression, performed by the video pipeline on said video frames.

22. (Previously Submitted) The method of claim 21, wherein said one or more image sensors are two image sensors, one of which provides input to said video pipeline and one of which provides input to said still image pipeline.

23. (Previously Submitted) The method of claim 22, wherein said two image sensors have different resolutions.

24. (Previously Submitted) The method of claim 21, wherein said video pipeline and said still image pipeline are separate hardware pipelines.